

STIC Search Report

STIC Database Tracking Number 144053

TO: Roy Potter

Location: JEF- 5A49

Art Unit: 2822

Wednesday, February 02, 2005

Case Serial Number: 08/043503

From: Darcy Bates Location: EIC 2800

JEF-4B68

Phone: 571-272-2540

darcy.bates@uspto.gov

Search Notes

Re: 08/043,503 US: 5,291,061

Attached are search results.

No U. S. litigation was found in searches of Lexis-Nexis and Questel-Orbit databases.

If more searching or explanation is needed, please let me know.

Thanks,
Darcy Bates



Litigation involving patent 5,291,061

Click on the docket number to view the docket. Click on the above patent number to view the patent.

Docket Case Heading Date Filed Date Retrvd

There are no cases involving this patent number.

Litigation involving patent RE36,613

Click on the docket number to view the docket. Click on the above patent number to view the patent.

Docket Case Heading Date Filed Date Retrvd

There are no cases involving this patent number.

Source: Legal > Area of Law - By Topic > Patent Law > Patents > U.S. Patents > Reissue Patents []

Terms: 36613 (Edit Search)

610127 (08) RE 36613 March 14, 2000

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

RE **36613** <

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Link to Claims Section

March 14, 2000

Multi-chip stacked devices

INVENTOR: Ball, Michael B. - Boise, Idaho, United States (US)

APPL-NO: 610127 (08)

FILED-DATE: February 29, 1996

GRANTED-DATE: March 14, 2000

ASSIGNEE-AT-ISSUE: Micron Technology, Inc., Boise, Idaho, United States (US), 02

ASSIGNEE-AFTER-ISSUE: January 21, 2003 - MERGER (SEE DOCUMENT FOR DETAILS)., MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAY BOISE IDAHO 83707-0006, Reel

and Frame Number: 013684/0806

LEGAL-REP: Trask, Britt & Rossa

PUB-TYPE: March 14, 2000 - Reissue Patent (E1)

PUB-COUNTRY: United States (US)

US-MAIN-CL: 257#777

US-ADDL-CL: 257#676, 257#686, 257#783, 257#784, 257#E23.047, 257#E23.052,

257#E25.013

CL: 257

SEARCH-FLD: 257#686, 257#777, 257#784, 257#676, 257#783

IPC-MAIN-CL: 7H 01L023#48

IPC ADDL CL: H 01L023#52, H 01L029#40, H 01L023#2

PRIM-EXMR: Arroyo, Teresa M.

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● Terms and Connectors ○ Natural Language 36613	Suggest Terms for My Search	between s connector	ectors to show relationships earch terms. (Hover over a for description. Click a to add to search.)
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Query/Command : file pluspat

Selected file: PLUSPAT

Search statement 1

Query/Command : re36613/pn

. SS 8: Results 1

Search statement 9

Query/Command : prt fu legalall max

1 / 1 PLUSPAT - @QUESTEL-ORBIT - image /

PN - USRE36613 E 20000314 [USRE36613]

TI - (E) Multi-chip stacked devices
PA - (E) MICRON TECHNOLOGY INC (US)

PAO - Micron Technology, Inc., Boise ID [US]

IN - (E) BALL MICHAEL B (US)

AP - US61012796 19960229 [1996US-0610127]

FD - Reissued: US043503 19930406 [1993US-0043503]

Reissue of: US5291061 - 19940301

PR - US61012796 19960229 [1996US-0610127]

US4350393 19930406 [1993US-0043503]

IC - (E) H01L-023/02 H01L-023/48 H01L-023/52 H01L-029/40

EC - H01L-023/495G4B H01L-023/495L

H01L-025/065S

PCL - ORIGINAL (O): 257777000; CROSS-REFERENCE (X): 257E23047

257E23052 257E25013 257676000 257686000 257783000 257784000

DT - Basic

CT - US4567643; US4984059; US4996587; US5012323; US5049976; JP56-62351; JP60-182731; JP62-126661; JP63-128736; JP1-28856; JP3-169062;

JP4-56262

STG - (E) Reissue

AB - A multiple stacked die device is disclosed that contains up to four dies and does not exceed the height of current single die

packages. Close-tolerance stacking is made possible by a low-loop-profile wire-bonding operation and thin-adhesive layer

between the stacked dies.

UP - 2000-13

1 / 1 LGST - ©EPO

PN - USRE36613 E 20000314 [USRE36613]

AP - US61012796 19960229 [1996US-0610127]

ACT - 20030121 US/AS-A

ASSIGNMENT

OWNER: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAYBOIS;

EFFECTIVE DATE: 19941028

MERGER; ASSIGNOR: MICRON SEMICONDUCTOR, INC.; REEL/FRAME: 013684/0806

UP - 2004-52

1 / 1 CRXX - @CLAIMS/RRX

AN · -3292154

E 20000314 [USRE36613] PN -36,613

Micron Technology Inc

E (Electrical) PT -

20030121 REASSIGNED ACT -

MERGER

Assignor: MICRON SEMICONDUCTOR, INC. DATE SIGNED: 10/28/1994

Assignee: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAY BOISE

IDAHO 83707-0006

Reel 013684/Frame 0806

Contact: TRASKBRITT BRICK G. POWER SALT LAKE CITY, UT 84110

UP -2003-33 URAS-2003-08-12

Search statement

Query/Command : fam re36613/pn

1 Patent Groups ** SS 9: Results 2

Search statement 10

Query/Command : famstate nonstop

1 / 2 PLUSPAT - @QUESTEL-ORBIT - image

PN -US5291061 A 19940301 [US5291061]

(A) United States patent STG -

(A) Multi-chip stacked devices TI -(A) MICRON SEMICONDUCTOR INC (US)

Micron Semiconductor, Inc., Boise ID [US] PAO -

IN -(A) BALL MICHAEL B (US)

(A) H01L-023/02 H01L-023/32 H01L-023/48 H01L-029/40 IC -

US4350393 19930406 [1993US-0043503] US4350393 19930406 [1993US-0043503] AP -

PR -

EC -H01L-023/495G4B H01L-023/495L

H01L-025/065S

ORIGINAL (O): 257686000; CROSS-REFERENCE (X): 257E23047 257E23052 257E25013 257676000 257782000 257783000

DT -Basic

1 / 1 LEGALI - ©EPO / US5291061 A 19940301 [US5291061] AP -US4350393 19930406 [1993US-0043503] ACTE-19930406 US/AS02-A ASSIGNMENT OF ASSIGNOR'S INTEREST OWNER: MICRON SEMICONDUCTOR, INC. 2805 EAST COLUMBIA ROAD; EFFECTIVE DATE: 19930401 19930406 US/AS02-A ASSIGNMENT OF ASSIGNOR'S INTEREST OWNER: BALL, MICHAEL B.; EFFECTIVE DATE: 19930401 19961001 US/RF-A REISSUE APPLICATION FILED EFFECTIVE DATE: 19960229 19991207 US/RF-A REISSUE APPLICATION FILED EFFECTIVE DATE: 19991021 20030121 US/AS-A ASSIGNMENT OWNER: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAYBOIS; EFFECTIVE DATE: 19941028 MERGER; ASSIGNOR: MICRON SEMICONDUCTOR, INC.; REEL/FRAME: 013684/0806 20030610 US/RF-A REISSUE APPLICATION FILED EFFECTIVE DATE: 20030116 UP 2004-52 2 / 2 PLUSPAT - @QUESTEL-ORBIT - image PN - USRE36613 E 20000314 [USRE36613] STG -(E) Reissue TI - (E) Multi-chip stacked devices PA -(E) MICRON TECHNOLOGY INC (US) PAO -Micron Technology, Inc., Boise ID [US] IN -(E) BALL MICHAEL B (US) IC -(E) H01L-023/02 H01L-023/48 H01L-023/52 H01L-029/40 US61012796 19960229 [1996US-0610127] US61012796 19960229 [1996US-0610127] ÀΡ PR -US4350393 19930406 [1993US-0043503] EÇ H01L-023/495G4B H01L-023/495L. H01L-025/065S ORIGINAL (O): 257777000; CROSS-REFERENCE (X): 257E23047 PCL -257E23052 257E25013 257676000 257686000 257783000 257784000 DT - Basic UP -2000-13 1 / 1 LEGALI - ©EPO USRE36613 E 20000314 [USRE36613] PN -AP -US61012796 19960229 [1996US-0610127] ACTE-20030121 US/AS-A ASSIGNMENT OWNER: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAYBOIS; EFFECTIVE DATE: 19941028 MERGER; ASSIGNOR: MICRON SEMICONDUCTOR, INC.; REEL/FRAME: 013684/0806

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2004-52

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- Use more common search terms. "Suggested Words and Concepts" are displayed on the search form when you click on Edit Search.

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Source: Legal > Area of Law - By Topic > Patent Law > Patents > U.S. Patents > Utility Patents 🔒

Terms: patno=5291061 (Edit_Search)

043503 (08) 5291061 March 1, 1994

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

5291061

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Link to Claims Section

March 1, 1994

Multi-chip stacked devices

REISSUE: This Patent was reissued on Mar. 14, 2000 as Reissue Patent Re 36,613. Reissued Application filed Oct. 19, 1999 (O.G. Dec. 7, 1999) Ex. Gp.: 2814; Re. S.N. 09/427,123Reissued Application filed Feb. 29, 1996 (O.G. Oct. 1, 1996) Ex. Gp.: 2503; Re. S.N. 08/610,127, (O.G. December 7, 1999) January 16, 2003 - Reissue Application filed Ex. Gp.: 2811; Re. S.N. 10/346,860 (O.G. June 10, 2003)

INVENTOR: Ball, Michael B. - Boise, Idaho, United States (US)

APPL-NO: 043503 (08)

FILED-DATE: April 6, 1993

GRANTED-DATE: March 1, 1994

ASSIGNEE-AT-ISSUE: Micron Semiconductor, Inc., Boise, Idaho, United States (US), 02

ASSIGNEE-AFTER-ISSUE: April 6, 1993 - ASSIGNMENT OF ASSIGNORS INTEREST., MICRON SEMICONDUCTOR, INC. 2805 EAST COLUMBIA ROAD BOISE IDAHO 83706, Reel and

Frame Number: 006513/0762

January 21, 2003 - MERGER (SEE DOCUMENT FOR DETAILS)., MICRON TECHNOLOGY, INC.

8000 SOUTH FEDERAL WAY BOISE IDAHO 83707-0006, Reel and Frame Number:

013684/0806

LEGAL-REP: Hopkins, French, Crockett, Springer & Hoopes

PUB-TYPE: March 1, 1994 - Utility Patent having no previously published pre-grant

publication (A)

PUB-COUNTRY: United States (US)

US-MAIN-CL: 257#686

US-ADDL-CL: 257#676, 257#782, 257#783, 257#E23.047, 257#E23.052, 257#E25.013

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Search statement 1

Query/Command : us5291061/pn

** SS 6: Results 1

Search statement 7

Query/Command : prt fu legalall max

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US5291061 A 19940301 [US5291061] PN -

ΤI (A) Multi-chip stacked devices

PA -(A) MICRON SEMICONDUCTOR INC (US)

Micron Semiconductor, Inc., Boise ID [US] PA0 -

IN · -

(A) BALL MICHAEL B (US) US4350393 19930406 [1993US-0043503] US4350393 19930406 [1993US-0043503] AP -PR -

(A) H01L-023/02 H01L-023/32 H01L-023/48 H01L-029/40 IC -

H01L-023/495G4B EC H01L-023/495L H01L-025/065S

ORIGINAL (O): 257686000; CROSS-REFERENCE (X): 257E23047

257E23052 257E25013 257676000 257782000 257783000

DT -

PCL -

US4567643; US4984059; US4996587; US5012323; US5049976; JP56-62351 CT

A; JP60-182731; JP62-126661; JP1-28856; JP3-169062

STG -(A) United States patent

AB -A multiple stacked die device is disclosed that contains up to four dies and does not exceed the height of current single die packages. Close-tolerance stacking is made possible by a

low-loop-profile wire-bonding operation and thin-adhesive layer between the stacked dies.

1 / 1 LGST - ©EPO

PN -US5291061 A 19940301 [US5291061]

AP -US4350393 19930406 [1993US-0043503]

ACT -19930406 US/AS02-A

ASSIGNMENT OF ASSIGNOR'S INTEREST

OWNER: MICRON SEMICONDUCTOR, INC. 2805 EAST COLUMBIA ROAD;

EFFECTIVE DATE: 19930401

19930406 US/AS02-A

ASSIGNMENT OF ASSIGNOR'S INTEREST

OWNER: BALL, MICHAEL B.; EFFECTIVE DATE: 19930401

19961001 US/RF-A

REISSUE APPLICATION FILED

EFFECTIVE DATE: 19960229

19991207 US/RF-A

REISSUE APPLICATION FILED EFFECTIVE DATE: 19991021

20030121 US/AS-A

ASSIGNMENT

OWNER: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAYBOIS;

EFFECTIVE DATE: 19941028

MERGER; ASSIGNOR: MICRON SEMICONDUCTOR, INC.; REEL/FRAME: 013684/0806

20030610 US/RF-A

REISSUE APPLICATION FILED EFFECTIVE DATE: 20030116

UP - 2004-52

1 / 1 CRXX - @CLAIMS/RRX

AN - 2450074

PN - 5,291,061 A 19940301 [US5291061]

PA - Micron Semiconductor Inc

PT - E (Electrical)

ACT - 19960229 REISSUE REQUESTED

ISSUE DATE OF O.G.: 19961001

REISSUE REQUEST NUMBER: 08/610127

EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2503

Reissue Patent Number: USRE36613

19991021 REISSUE REQUESTED
ISSUE DATE OF O.G.: 19991207
REISSUE REQUEST NUMBER: 09/427123
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2814

Reissue Patent Number:

20030116 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20030610
REISSUE REQUEST NUMBER: 10/346860
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2811

Reissue Patent Number:

20030121 REASSIGNED

MERGER

Assignor: MICRON SEMICONDUCTOR, INC. DATE SIGNED: 10/28/1994

Assignee: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAY BOISE

IDAHO 83707-0006

Reel 013684/Frame 0806

Contact: TRASKBRITT BRICK G. POWER SALT LAKE CITY, UT 84110

UP - 1999-00 **UACT-** 2003-06-10 **URAS-** 2003-08-12

Search statement 7

Query/Command : fam us5291061/pn

1 Patent Groups ** SS 7: Results 2

Search statement 8

Query/Command : famstate nonstop

1 / 2 PLUSPAT - @QUESTEL-ORBIT - image

PN -US5291061 A 19940301 [US5291061]

STG -(A) United States patent

TI -(A) Multi-chip stacked devices

PA -(A) MICRON SEMICONDUCTOR INC (US)

PA0 -Micron Semiconductor, Inc., Boise ID [US]

IN -(A) BALL MICHAEL B (US)

IC -(A) H01L-023/02 H01L-023/32 H01L-023/48 H01L-029/40

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H01L-023/495G4B H01L-023/495L H01L-025/065S

PCL -ORIGINAL (O): 257686000; CROSS-REFERENCE (X): 257E23047

257E23052 257E25013 257676000 257782000 257783000

DT -Basic

1 / 1 LEGALI - ©EPO

PN -US5291061 A 19940301 [US5291061]

AP -US4350393 19930406 [1993US-0043503]

19930406 US/AS02-A ACTE-

ASSIGNMENT OF ASSIGNOR'S INTEREST

OWNER: MICRON SEMICONDUCTOR, INC. 2805 EAST COLUMBIA ROAD;

EFFECTIVE DATE: 19930401

19930406 US/AS02-A

ASSIGNMENT OF ASSIGNOR'S INTEREST

OWNER: BALL, MICHAEL B.; EFFECTIVE DATE: 19930401

19961001 US/RF-A

REISSUE APPLICATION FILED

EFFECTIVE DATE: 19960229

19991207 US/RF-A

REISSUE APPLICATION FILED

EFFECTIVE DATE: 19991021

20030121 US/AS-A

ASSIGNMENT

OWNER: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAYBOIS;

EFFECTIVE DATE: 19941028

MERGER; ASSIGNOR: MICRON SEMICONDUCTOR, INC.; REEL/FRAME: 013684/0806

20030610 US/RF-A

REISSUE APPLICATION FILED

EFFECTIVE DATE: 20030116

UP - 2004-52

- 2 / 2 PLUSPAT @QUESTEL-ORBIT image
- PN USRE36613 E 20000314 [USRE36613]

STG - (E) Reissue

TI - (E) Multi-chip stacked devices
PA - (E) MICRON TECHNOLOGY INC (US)

PAO - Micron Technology, Inc., Boise ID [US]

IN - (E) BALL MICHAEL B (US)

IC - (E) H01L-023/02 H01L-023/48 H01L-023/52 H01L-029/40

AP - US61012796 19960229 [1996US-0610127] **PR** - US61012796 19960229 [1996US-0610127]

US4350393 19930406 [1993US-0043503]

EC - H01L-023/495G4B H01L-023/495L H01L-025/065S

PCL - ORIGINAL (O): 257777000; CROSS-REFERENCE (X): 257E23047

257E23052 257E25013 257676000 257686000 257783000 257784000

DT - Basic **UP** - 2000-13

1 / 1 LEGALI - ©EPO

PN - USRE36613 E 20000314 [USRE36613]

AP - US61012796 19960229 [1996US-0610127]

ACTE- 20030121 US/AS-A

ASSIGNMENT

OWNER: MICRON TECHNOLOGY, INC. 8000 SOUTH FEDERAL WAYBOIS;

EFFECTIVE DATE: 19941028

MERGER; ASSIGNOR: MICRON SEMICONDUCTOR, INC.; REEL/FRAME: 013684/0806

UP - 2004-52

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